



- Dual Output
- SIP or DIP Package
- 1000 VDC Isolation
- Short Circuit Protection
- -40 °C to +85 °C Operation
- MTBF >1.1 MHrs
- 3 Year Warranty

Specification

Input

- | | |
|----------------------------------|---|
| Input Voltage Range | • Nominal ±10% ⁽⁵⁾ |
| Input Reflected Ripple Current | • 20 mA pk-pk (5 Hz to 20 MHz with 12 µH) |
| Input Reverse Voltage Protection | • None |

Output

- | | |
|-------------------------|---|
| Output Voltage | • See table |
| Minimum Load | • None ⁽⁶⁾ |
| Line Regulation | • 1.2%/1% Δ Vin |
| Load Regulation | • ±10% 20-100% load change
(3.3 V models ±20%) |
| Setpoint Accuracy | • ±3% |
| Ripple & Noise | • 75 mV pk-pk max, 20 MHz bandwidth |
| Temperature Coefficient | • 0.02%/°C |
| Maximum Capacitive Load | • ±100 µF |

General

- | | |
|-----------------------|---|
| Efficiency | • See table |
| Isolation Voltage | • 1000 VDC minimum |
| Isolation Resistance | • 10 ⁹ Ω |
| Isolation Capacitance | • 60 pF typical |
| Switching Frequency | • Variable, 80 KHz typical |
| MTBF | • >1.12 MHrs to MIL-HDBK-217F
at 25 °C, GB |

Environmental

- | | |
|-----------------------|---------------------|
| Operating Temperature | • -40 °C to +85 °C |
| Storage Temperature | • -40 °C to +125 °C |
| Case Temperature | • 100 °C max |
| Cooling | • Convection-cooled |

Notes

1. Replace 'S' in model number with 'D' for DIP package.
2. SIP 48 Vin models, dimension is 0.28 (7.20) max.
3. DIP 48 Vin models, dimension is 0.27 (6.88) max.
4. Outputs power-trade.
5. For 48 V models a 10 µF capacitor is required between +Vin and -Vin pins.
6. Operation at no load will not damage unit but it may not meet all specifications.
7. All dimensions in inches (mm).
8. Pin pitch tolerance: ±0.014 (±0.35)
9. Case tolerance ±0.02 (±0.5)
10. Weight: SIP 0.004 lbs (2.2 g), 48 V SIP 0.005 lbs (2.7 g), DIP 0.005 lbs (2.4g)

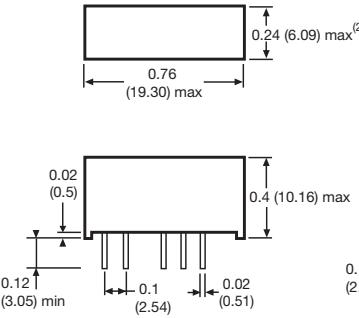
	Input Voltage ⁽⁵⁾	Output Voltage	Output Current ⁽⁴⁾	Efficiency	Model Number ⁽¹⁾
5 VDC	3.3 VDC	±5.0 V	±100 mA	66%	IA0305S ⁽⁸⁾
		±3.3 V	±151 mA	65%	IA0503S ⁽⁸⁾
		±5.0 V	±100 mA	74%	IA0505S ⁽⁸⁾
		±9.0 V	±55 mA	77%	IA0509S ⁽⁸⁾
		±12.0 V	±42 mA	78%	IA0512S ⁽⁸⁾
		±15.0 V	±33 mA	80%	IA0515S ⁽⁸⁾
		±24.0 V	±21 mA	80%	IA0524S ⁽⁸⁾
12 VDC		±3.3 V	±151 mA	66%	IA1203S ⁽⁸⁾
		±5.0 V	±100 mA	75%	IA1205S ⁽⁸⁾
		±9.0 V	±55 mA	76%	IA1209S ⁽⁸⁾
		±12.0 V	±42 mA	78%	IA1212S ⁽⁸⁾
		±15.0 V	±33 mA	80%	IA1215S ⁽⁸⁾
		±24.0 V	±21 mA	76%	IA1224S ⁽⁸⁾
		±3.3 V	±151 mA	68%	IA2403S ⁽⁸⁾
24 VDC		±5.0 V	±100 mA	74%	IA2405S ⁽⁸⁾
		±9.0 V	±55 mA	76%	IA2409S ⁽⁸⁾
		±12.0 V	±42 mA	78%	IA2412S ⁽⁸⁾
		±15.0 V	±33 mA	78%	IA2415S ⁽⁸⁾
		±24.0 V	±21 mA	78%	IA2424S ⁽⁸⁾
		±3.3 V	±151 mA	60%	IA4803S
		±5.0 V	±100 mA	70%	IA4805S ⁽⁹⁾
48 VDC		±9.0 V	±55 mA	72%	IA4809S
		±12.0 V	±42 mA	74%	IA4812S ⁽⁹⁾
		±15.0 V	±33 mA	74%	IA4815S
		±24.0 V	±21 mA	70%	IA4824S

⁽¹⁾ Available from Farnell. See pages 266-269.

⁽⁸⁾ Available from Newark. See pages 270-272.

Mechanical Details

SIP Package



DIP Package

